



THE UNITED STATES PATENT AND TRADEMARK OFFICE

#3/Electronic
#110/02
1/18

In re the Application of:

Masaki HIRASE, et al.

Serial No.: **09/908,941**

Group Art Unit: **2815**

Filed: **July 20, 2001**

Examiner: **NGUYEN, Joseph H.**

For: **SEMICONDUCTOR DEVICE AND METHOD FOR
MANUFACTURING THE SAME**

**RESPONSE TO THE RESTRICTION REQUIREMENT
DATED MARCH 4, 2002**

Commissioner for Patents
Washington, D.C. 20231

Date: March 4, 2002

RECEIVED
MAR 28 2002
TECHNOLOGY CENTER 2800

Sir:

This paper is submitted in response to the Official Action dated March 4, 2002.

In the Action, restriction is required between Group (I), Claims 1-2, drawn to a semiconductor device; and Group (II), Claims 3-7, drawn to a method of manufacturing a semiconductor device.

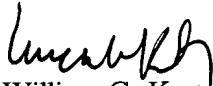
Applicants hereby elect the subject matter of Group (II), Claims 3-7 for prosecution in this application. This election is made without traverse, it being understood that the applicants' rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicants hereby petition for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please charge
our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



William G. Kratz, Jr.
Attorney for Applicants
Reg. No. 22,631

Atty. Docket No. 010917
1725 K Street, N.W., Suite 1000
Washington, DC 20006
Tel: (202) 659-2930
Fax: (202) 887-0357
WGK/nrp